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PA7200: A PA-RISC Processor with Integrated High Performance MP **Bus** Interface
G Kurpanek, K Chan, J Zheng, E DeLano, W Bryg - COMPCON, 1994 - ieeexplore.ieee.org
... The **bus** interface and the assist cache are com- pletely new designs ... When the complete
line is available, one **data** cache cycle is used to **insert** the line ...
Cited by 52 - Web Search - ieeexplore.ieee.org

[PS] A Freely Configurable Audio-Mixing Engine with Automatic Loadbalancing
M Rosenthal, M Klebl, A Gunzinger, G Troster - 98th AES Convention, Paris. AES Convention
Preprint I, 1995 - ife.ee.ethz.ch
... Every controller is programmed to **insert data** from its PE at a ... possible clock frequency
on the ring **bus**, is at the ... which is responsible for the **data**-ow between ...
Cited by 1 - View as HTML - Web Search

A Reduced-swing **Data** Transmission Scheme for Resistive **Bus** Lines in VSLIs
M Ikeda, K Asada - EDAC-ETC-EUROASIC, 1994 - ieeexplore.ieee.org
... **data** transmission module. ... Taking control signals skew and **bus** delay deviation into
account, we have to **insert** an additional delay amount in feed back loop ...
Cited by 1 - Web Search - ieeexplore.ieee.org

High-density and high-pin-count flexible SMD connector for high-speed **data bus**
S Sasaki, T Kishimoto - IEEE CHMT EUR INT ELECTRON MANUF TECHNOL SYMP, IEEE, ...,
1993 - ieeexplore.ieee.org
... When comparing the force required to **insert** all contacts ... applicable to the wide bit
data bus system. ... used for simulation, which connects **two** copper polyimide ...
Cited by 1 - Web Search - ieeexplore.ieee.org - csa.com - csa.com

IEEE 1394: A Ubiquitous **Bus**
G Hoffman, D Moore, S Inc - COMPCON, 1995 - doi.ieeecs.org
... and l provide power for devices connected to the **bus**. IEEE 1394 provides **data** transport
and power-a great ... use even when the user must blindly **insert** them into ...
Cited by 3 - Web Search - doi.ieeecomputersociety.org - ieeexplore.ieee.org - portal.acm.org

The Stratus Architecture
S Webber, J Beirne - FTCS, 1991 - ieeexplore.ieee.org
... system need to be disabled to remove or **insert** a board. ... **bus** request of up to 64 bits
of **data** on every ... The **bus** is actually viewed as **two** independent buses, **Bus** ...
Cited by 26 - Web Search - ieeexplore.ieee.org

ISSUES CONCERNING THE IMPLEHENIATION OF HIGH SPEED OPTICAL DATABUS
SYSTEMS
DR Brown - Future Military Avionic Architectures, IEE Colloquium on, 1990 - ieeexplore.ieee.org
... are proposed in which each LRU acts as a "drop and **insert**" station on a ring **bus**. ...
so that in the event of failure in an LRU the **data bus** will by—pass ...
Web Search - ieeexplore.ieee.org

A Novel Power & Communication Bus Concept
B Inniss, AAP Syst, Q St Laurent - Telecommunications Energy Conference, 2000. INTELEC. Twenty-
..., 2000 - ieeexplore.ieee.org
... Connector alignment and lead-in into DC/**data bus** ... The lead-in blade is hollowed
out, in order to **insert a data** connector circuit pack. ...
Web Search - ieeexplore.ieee.org

Fast Parallel Selection on the Linear Array with Reconfigurable Pipelined Bus System
Y Han, Y Pan, H Shen, EDS Inc, MI Troy - Proceedings of the The 7th Symposium on the Frontiers of
..., 1999 - doi.ieeecs.org
... Novel features and ecient basic **data** move- ment operations ... In the LARPBS, we **insert**
an optical switch on each section of the transmitting **bus** and receiving ...
Cited by 3 - Web Search - ieeexplore.ieee.org - doi.ieeecomputersociety.org - portal.acm.org - all 6
versions »

SPAID: software prefetching in pointer- and call-intensive environments
MH Lipasti, WJ Schmidt, SR Kunkel, RR Roediger - PROC ANNU INT SYMP
MICROARCHITECTURE, 1995 - portal.acm.org
... on systems that are already memory **bus** bandwidth-lim ... Once again, this **data** reinforces
the need for ... significantly as we attempt to **insert** multiple prefetches per ...
Cited by 73 - Web Search - ieeexplore.ieee.org - ece.wisc.edu - american.cs.ucdavis.edu - all 7 versions
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Instrumentation of the central solenoid model coil and the CS insert
CY Gung, PC Michael, NN Martovetsky, T Isono, Y ... - IEEE Transactions on Appiled
Superconductivity, 2001 - ieeexplore.ieee.org
... **insert** were instrumented to obtain valuable **data** about their ... I], outer module [2],
superconducting (SC) **bus** bars, and ... The CS **Insert** [3] is the first of three ...
Cited by 2 - Web Search - psfcwww2.psfc.mit.edu - psfc.mit.edu - csa.com - all 5 versions »

The DISTO Data Acquisition System at SATURNE
VT Salabura, F Tosello, SE Vigdor, G Zosi - IEEE TRANSACTIONS ON NUCLEAR SCIENCE, 1998
- ieeexplore.ieee.org
... **bus** VIC 8250 is used to **insert** the Silicon Graphics Workstation on to the VME-**bus**,
and the ... 20 GBytes with hardware compression, high **data** transfer rates ...
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THE DEVELOPMENT OF AUDIO AND VIDEO SIGNAL PROCESSING LSI FOR SD-DVC
T Kashiro, E Yamauchi - IEEE Transactions on Consumer Electronics, 1995 - ieeexplore.ieee.org
... (l)All the required **data** for digital interface exist on DVC **BUS**. ... D—I/F transmit ode
(c) **Insert** Audio (d) Digital IN Recording Fig.9 **Data** direction on ...
Web Search

Improving the Data Cache Performance of Multiprocessor Operating Systems
C Xia, J Torrellas - The 1996 2 nd International Symposium on High-Performance ..., 1996 -
doi.ieeecomputersociety.org
... Using this methodology, we **insert** one escape load at the ... stall include deeper write
buffers and higher **bus** and memory ... number 1-3% of the original **data** misses. ...
Cited by 3 - Web Search - doi.ieeecs.org - ieeexplore.ieee.org - cs.uiuc.edu - all 14 versions »

The DISTO **Data** Acquisition System at SATURNE
F Balestra, Y Bedfer, R Bertini, LC Bland, A ... - Arxiv preprint hep-ex/9712006, 1997 - arxiv.org
... **bus** VIC 8250 is used to **insert** the Silicon Graphics Workstation on the VME-**bus**,
and the ... of 20 GBytes with hardware compression, high **data** transfer rates ...
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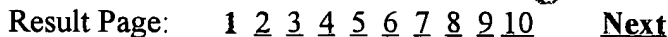Description of a MIL-STD-1553 B **data bus** Ada driver for the LeRC EPS Testbed
MA Mackin - 1995 - ntrs.nasa.gov
... Each of these computers communicates with **two** other computers which act
as subsidiary controllers. ... Page 6. 1553B **Data Bus** Operation ...
View as HTML - Web Search - csa.com - Library Search

The design of an inexpensive undergraduate **data** communications laboratory
WD Smith - ACM SIGCSE Bulletin, 1991 - portal.acm.org
... **two** or more star, ring or **bus** topologies, as ... The student can **insert** the monitor between
any **two** stations that are being used for **data** communications experiments ...
Cited by 4 - Web Search - portal.acm.org

On Dynamically Establishing and Terminating Isochronous Message Streams in **Dual-Bus** Networks
KS Tsoi, CJ Hou, CC Han - Global Telecommunications Conference, 1996. GLOBECOM'96.' ..., 1996
- ieeexplore.ieee.org
... configuration under consideration consists of **two** unidirectional buses ... taps which
enable transmission on each **bus**. **Data** transmission on both buses is slotted. ...
Web Search - ieeexplore.ieee.org

Internet Streaming SIMD Extensions
S Thakkar, T Huff - IEEE Computer, 1999 - hotchips.org
... Scalar Single ↔ 32-bit Signed Integer **Two** LS Packed Single ↔ **Two** 32-bit Signed
Integer (MMX ... SST: 7/12/99 Rev0.9 **Data** Movement Instructions MOVPS/MOVEUPS: ...
Cited by 52 - View as HTML - Web Search - dx.doi.org - ieeexplore.ieee.org - portal.acm.org - all 7
versions »

Design for test(DFT) for an embedded ARM system on a chip
P Furlong, D Breathnach, J Daly - IEE COLLOQ DIG, 1998 - ieeexplore.ieee.org
... System **Bus**) and AFB (Advanced Peripheral **Bus**) which are ... clock to the design with
possible **data** transfers from ... The **two** RAMs are each tested separately by built ...
Web Search - ieeexplore.ieee.org - csa.com

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An Integrated Controller Module for Distributed Control of Power Electronics
JA du Toit, AD Le Roux, JHR Enslin - IEEE APEC'98 Proceedings, 1998 - ieeexplore.ieee.org
... a resistive shunt, hall effect current **sensor**, current :;ense ... to the center tap of
the dc- **bus** capacitors. ... the standard three phase four wire **topology**, but the ...
Cited by 5 - Web Search - ieeexplore.ieee.org

A MICROCOMPUTER NETWORK FOR AUTOMATED MINING
WH Schiffbauer - Industry Applications Society Annual Meeting, 1990., ..., 1990 - ieeexplore.ieee.org
... node to multiple slave nodes in a multidrop **topology**. ... This maximizes the serial **bus**
utilization and thus system ... it provides access to all **sensor**-based machine ...
Web Search - ieeexplore.ieee.org

[BOOK] Embedded System Applications
C Baron, JC Geffroy, G Motet - 1997 - print.google.com
... interface (API) to enable other third party CASE tools to **insert data** for display ...
LMAS has provided its own custom capabilities to tie these **two** product suites ...
Cited by 5 - Web Search - Library Search

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